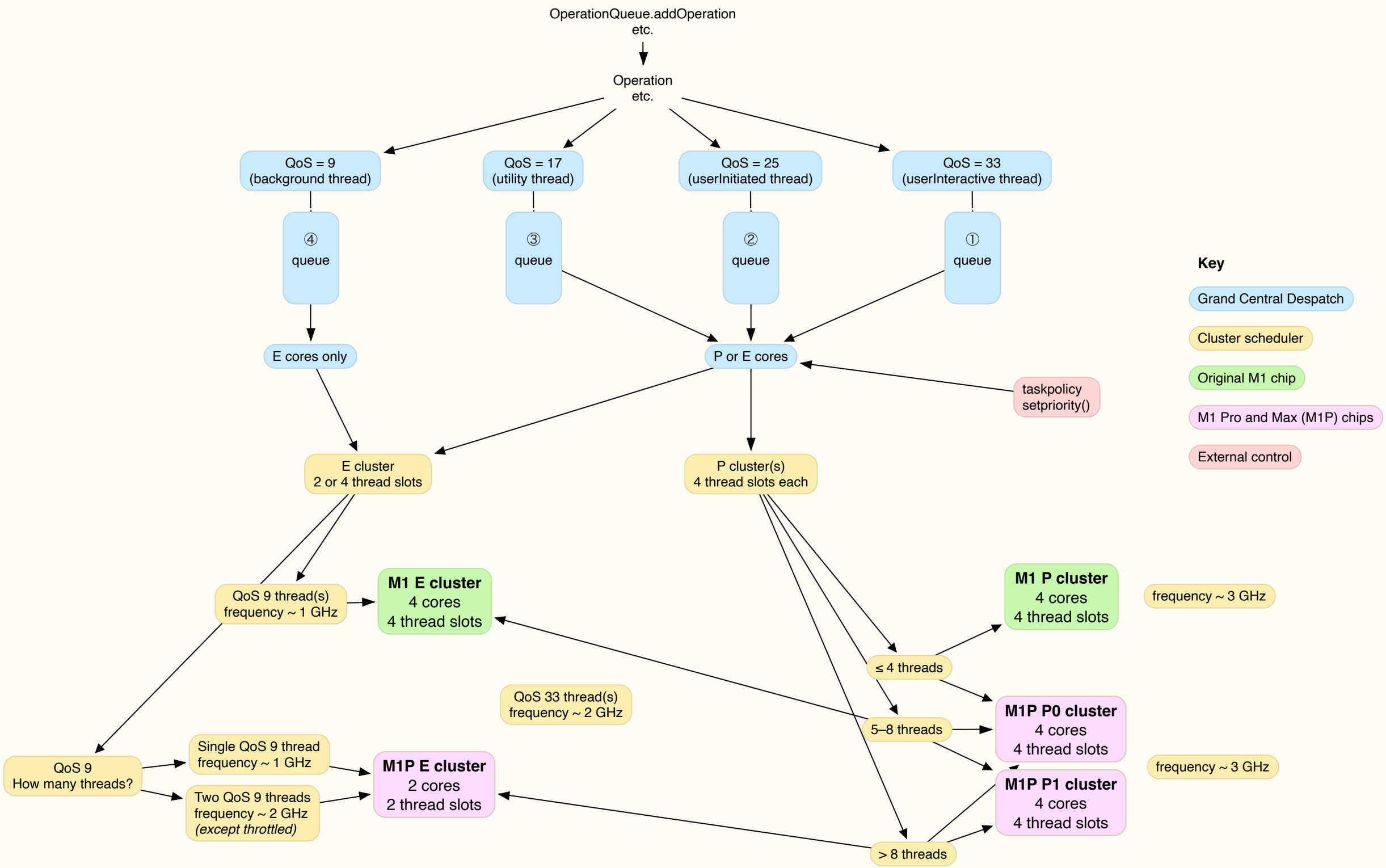Scheduling of Threads on M1 series Chips *(draft 0.2)*